



Saved

- 81: (118757) (nonvolatile or non-volatile or (non adj volatile)) adj2 memory
- 82: (67976) buffer\$4 adj2 memory
- 83: (1763942) writ\$4 or program\$4
- 84: (112368) stor\$4 near3 capacity
- 85: (2114) 82 same 84
- 86: (1463) 82 with 84
- 87: (679) 86 same 83
- 88: (29) 87 same 81
- 89: (416) 86 same 83
- 910: (63) 81 and 89
- 912: (7012851) control\$5
- 913: (214) 89 same 812
- 914: (189) 813 not (88 or 810)
- 915: (1036) 365/185.28
- 916: (1660) 365/185.11
- 917: (395418) @RLAD<19910726"
- 918: (19) 813 and 817
- 919: (16) 815 and 817
- 920: (20) 86 same 81
- 922: (130) 86 and 817
- 923: (50) 87 and 817
- 924: (29) 823 not (820 or 818 or 819 or 810)
- 926: (5394) 81 and 82 and 83
- 927: (5310) 812 and 826
- 928: (267) 817 and 827
- 929: (262) 828 not (823 or 820 or 818 or 819 or 810)
- 944: (34) (LOFGREN near2 KARL).ln.
- 945: (10) (STAI near2 JEFFREY).ln.
- 946: (55) (GUPTA near2 ANIL).ln.
- 947: (488) (NORMAN near2 ROBERT).ln.
- 948: (88) (MEHROTRA near2 SANJAY).ln.

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DBs US-PG-PUB; USPAT; EPO; JPO; DERWENT; ISM_TOB

P Ends

Default operator: OR

Highlight all hit terms initially

BRS form ISM form Image Text HTML

	U	Document ID	Source	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor
1	<input type="checkbox"/>	US 6179687 A	USPAT	19930112	20	Semiconductor memory device containing a cache and an oper	711/118	365/193; 365/49		Hidaka; Hideto et al.

HLS Details HTML

Ready

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